

# Deepak M

**Company:**

UST Global. India.

Bangalore.

Email Id: deepakmano555@gmail.com.

Contact No : 8248740133.

**Residence:****CAREER OBJECTIVE :**

To secure a challenging position where I will use all my skills and education with a company who offer opportunity for advancement and career growth while upgrading myself and organization with the latest technologies.

**TECHNICAL CAPABILITIES :**

- **Have Good Experience in Testing (Manual Testing).**
- Hands-on Experience in **Testcase Execution ,Defect Tracking, Finding Scenarios and Testcase Creation**
- Good Knowledge in handling **Test Case** and **Defect Tracking** for Silicon with Windows Systems.
- Understanding, Analyzing and Streamlining **Test Requirements.**
- Expertise in handling all Products Handling Customer issues and reprocessing Issues and raising **DTS(HSD)** and tracking results.
- Expertise in handling tools like **Eclipse, JAMA, Phoenix, HSD**
- Expertise in Bug Reporting & Tracking Process, Bug tracking tools like **HSD** ( High Speed Data).
- Expertise in collecting logs using tools like **WBB tool ,Eclipse Trace HUB,** tool for intel core Processor and **TTK** tools for Silicon devices.
- Expertise in **Functional Testing (Debug and Trace and Thunderbolt domain ), Exploratory Testing, Ad-hoc Testing, Sanity Testing**
- Experience of discussion with Clients, Development Team, Understanding Requirements & involved with them till version sharing, Daily standup meeting and Whiteboard Analysis
- Knowledge on **Java** (OOPS Concept ,Collection Framework, Exception Handling, Casting ,JDBC).
- **Selenium Framework** (locators ,X-Path ,Annotations ,TestNG ,All Frameworks).
- **SQL** (SQL Quires, DDL ,DML, TCL, Joins, Sub-Quires, Grouping, Functions).

## **TECHNICAL SKILLS :**

- **Technical Skills:** Silicon Validation, Manual Testing ,Automation Script Running
- **Technology/Domain:** Debug and trace, TBT(Thunderbolt).
- **Operating Systems:** Windows.
- **IDE:** Eclipse, ISD, Eclipse trace Hub, SX cycling.
- **Testing Tools:** JAMA, phoenix, HSD.
- **Language Known:** Java ,Selenium Framework ,Manual Testing
- **Database Known:** SQL, MySQL.

## **PROFESSIONAL SUMMARY :**

**Company Name: UST Global. India**

Designation: Validation Engineer(ManualTesting)

Experience: 0.9+ Years

**Client: UST Global. India (Intel).**

## **PROJECT: DELIVERY (Intel Core Processor).**

**Team size: 20 members**

**Products Delivered: Silicon Core Processor (10<sup>th</sup> gen. 11<sup>th</sup> gen)**

**Tools:** Jama, Phoenix, HSD(Defect Tracking System),ISD(intel System Debugger),WinDbg.

## **Key Responsibilities:**

- Collecting requirements from Windows according to the Country region in collaboration with Software Manager, Test manager and Software Developer.
- Testing the specific requirements and General requirements corresponding to Silicon.
- Running the **Functional testing** for the devices based on Configuration and Silicon with Windows operating Systems.
- Finding **Regression**, Executing **Sanity Testcase** ,**Exploratory Testcase** and **Ad-hoc Testcase**
- Testing, **Debug and Trace** and **Thunderbolt** Domain on Silicon related using Functional test cases and Running **Stress** and **Stability Testcase** using Third-party Applications.
- **Found 21+ defects** successfully and raised HSD (High Speed Data) to fix the issues noticed.
- Finding Exploratory Testing, Ad-hoc testing Scenarios and Defect Tracking ,Isolation, Finding Root cause Analysis.

## **Professional Training :**

- **Software Testing and Development** Course Completed in Q'Spiders Bangalore.

**Skills :** Manual Testing, SQL, Java, Selenium, Spring Framework.

## **EDUCATIONAL QUALIFICATIONS :**

- Completed **B.E in Electronics and Communication Engineering** passed with **66%\*** aggregate in the year **2019** from **Bharathidasan Engineering College, Vellore**, Approved from **Anna University, Chennai**.
- Completed **HSC(Bio-Math's)** with **70.00%** aggregate in the year **2015** from **Don Bosco Hr. Sec school, Vellore** approved from **State Board of Tamilnadu**.
- Completed **SSLC** with **85%** in the year **2013** from , **Don Bosco Hr. Sec school, Vellore** approved from **State Board of Tamilnadu**.

## **PERSONAL INFORMATION :**

Fathers Name : Manogaran. J

Gender : Male.

Date of Birth : 03-06-1998.

Nationality : Indian.

Marital Status : Single.

Languages known: English ,Tamil and Telugu.

## **DECLARATION :**

I hereby declare that all the particulars mentioned above are true to the best of my knowledge and belief.

Date:

Place: Bengaluru

(Signature)

Deepak M