Mohammad Pervez Hussain Khan Pathan

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To work for an organization which provides me an opportunity to improve my skills, knowledge and grow along with the objective of organization.

Personal Details

Date of Birth: 1998-08-08
Height: 5.10
Eligible to work in: India
Highest Career Level: No Experience / Student
Industry: Electrical Engineering, Software Development
Total years of experience: 0

Work Experience

Student Trainee

ABC FOR Technology Training
October 2019 to Present

Java and Testing

Education

Bachelor's in Electrical and Electronics Engineering

MJR College of Engineering and Technology - Pileru, Andhra Pradesh
2015 to 2020

Higher Secondary(12th Pass) in Maths, Physics, Chemistry

MDS Junior college, Pileru, Chittoor district, Andhrapradesh state - Pileru, Andhra Pradesh
2013 to 2015

Secondary(10th Pass)

ZP High School(Telugu) Gurramkonda - Gurramkonda, Andhra Pradesh
2013

Skills / IT Skills

• Core Java(Methods, Arrays, Objects, oops concept, Exception Handling, Multithreading)

• J2EE(JDBC, SERVELETS)

• Testing

• Sql

Languages

- English - Expert
- Urdu - Expert

Online Profile

http://www.linkedin.com/in/pervez-hussain-06233419b

Projects / Papers Presented

Project work on Three Phase Modular Multilevel DC-DC converter for PowerElectronic transformer application

April 2019

A three-phase modular multilevel dc-dc converter is proposed and analyzed for power electronic transformer (PET) applications. Similar to a dual active bridge converter, the proposed converter comprises two three-phase inverters/rectifiers, coupled via a medium frequency (MF) transformer. The modular multilevel converter structure is used in the medium-voltage side to meet the high-voltage requirements and to reduce the dv/dt stress on the MF transformer. The frequency of the voltage through MF transformer (isolation frequency) is the same as the switching frequency of the power semiconductor devices, and zero-voltage switching-ON can be achieved for all the devices. With only one three-phase MF transformer, the proposed topology can greatly simplify the transformer design compared with the existed input- series output-parallel dc-dc converter structures for PET applications. A dual-phase-shift method is presented to control the output power and to balance the sub module capacitor voltages. Simulations and experimental results are provided to validate the theoretical analysis