

JYOTHI M S

B. E (Electronics and Communication Engg), M.Tech (VLSI Design and Embedded Systems)



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📍 Udayagiri, Hassan, Karnataka-573201

RESUME OBJECTIVE

To work for an organization which provides me the opportunity to improve my skills and knowledge to grow along with the organization objective and seeking a position to utilize my skills and abilities in an organization that offers security, self-assurance and Professional growth while being resourceful, innovative and flexible.

TECHNICAL SKILLS

Python

SQL

HTML CSS JS Bootstrap

Django

Flask

Java

MATLab

C and C++

Verilog HDL

SOFT SKILLS

Strong Work Ethic

Problem Solving

Leadership

Handling Pressure

Time Management

LANGUAGES

English – read-write-speak

Kannada – read-write-speak

Hindi – read-write-speak

EXPERIENCE

ASSISTANT PROFESSOR (Guest)

Govt. Engineering Collage, Hassan / August 2015 – February 2020

- *Own projects* : "Low power with improved noise margin for domino cmos nand gate", "SMS controlled notice board", "Home automation", "Accident avoidance system in blind curves".
- *Projects guided* : "Library data managing device", "RFID based office security system", "Lab automation using Bluetooth technology", "Mobile phone detector".

EDUCATION

MTech / VLSI Design and embedded systems

SIET, Tumkur, under VTU, 2013 – 2015

- Aggregate percentage 80.00%
- *National Conference* : "Feedback Shift Register Based cryptographic System using LBIST Method", presented in Kalpataru Institute of Technology on 28th and 29th April 2015.
- *International Journal* : "Feedback Shift Register Based cryptographic System using LBIST Method" published in International Journal of Engineering Trends and Technology (IJETT) – Volume23 Number 4- May 2015. *Paper Link*: <http://ijettjournal.org/archive/ijett-v23p235-2>

BE / Electronics and Communication
GEC, Hassan, Under VTU, 2009– 2013

- Aggregate percentage 62.00%

Pre University Education/PCMB
KSPU Board, 2007-2009

- percentage 62.00%

SSLC
KSEB, 2007

- percentage 83.20%